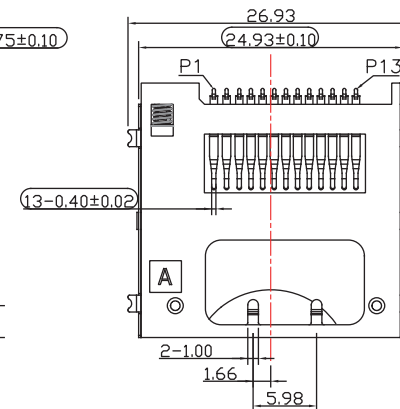
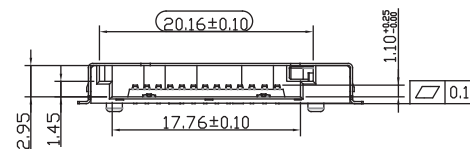
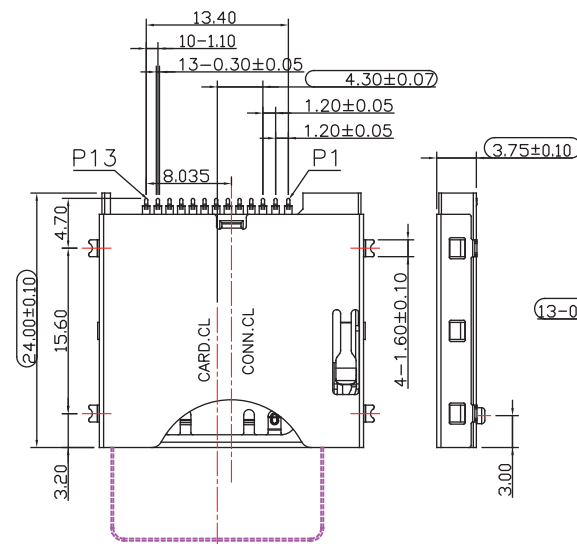
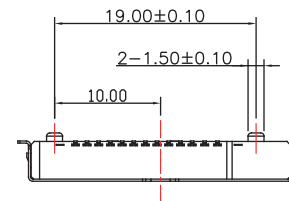
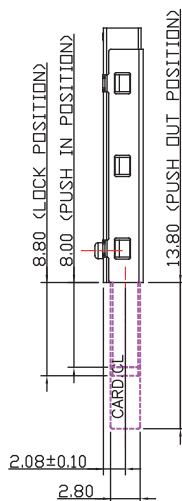
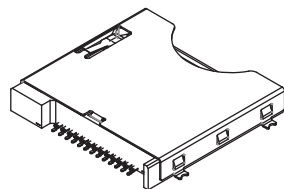
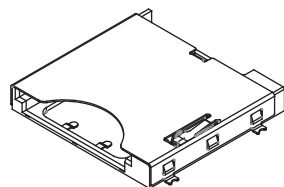


RECOMMENDED P.C.B LAYOUT  
TOLERANCE IS:  $\pm 0.05$

- PAD AREA
- KEEP OUT AREA
- NO COPPER AREA (NO Trace & Via GND)
- GND PATTERN ONLY



PIN DESIGN

13	GND	Ground
12	PETp0	PCIe Gen3 Tx singal
11	PETn0	PCIe Gen3 Tx singal
10	GND	Ground
9	PERp0	PCIe Gen3 Rx singal
8	PERn0	PCIe Gen3 Rx singal
7	GND	Ground
6	REFCLK+	Reference Clock input
5	REFCLK-	Reference Clock input
4	INS#	Media detection and power control
3	CLKREQ#	Indication of REFCLK to Host
2	+3.3V	3.3V Power Supply
1	PERST#	PCIe Express function reset

**DELCK**  
we move the world

Title: CFexpress TypeA Connector  
w/o shell Ground

Item No. **66328**

Barcode 404361966328

File No.